



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Terumasa Kitahara

Application No.: 10/040,460

Filed: January 9, 2002

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE WITH ENHANCED LAYOUT

Group Art Unit: 2818

Examiner: Thinh T. Nguyen

Docket No.: 024016-00022

**AMENDMENT UNDER 37 C.F.R. § 1.121**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

July 21, 2003

Sir:

**INTRODUCTORY COMMENTS**

In reply to the Office Action mailed February 19, 2003, the period for response being extended by the attached one-month Petition for Extension of Time, please amend the above-identified application as follows: